

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	
WRIGHT ET AL)	
)	Atty. Docket No.
Patent No. 7,103,049)	72150
)	
Issue Date: 09/05/2006)	
)	
For: MECHANISM FOR PROVIDING OCTET)	
ALIGNMENT IN SERIAL ATM DATA)	
STREAM)	
)	

REQUEST FOR CERTIFICATE OF CORRECTION OF OFFICE MISTAKE
UNDER RULE 1.322(a)

EFILED
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of Rule 1.322(a) of the Rules of Practice, the Patent Office is respectfully requested to correct matters in the above-identified patent in accordance with the Certificate of Correction Form PTO-1050 attached hereto.

The Patent Office is requested to issue a Certificate of Correction on the above-referenced Patent Document, and to place such a Certificate of Correction in the file, so that such will appear on any copies of the patent, which are ordered in the future.

The corrections being made are of consequence to provide the proper recitation in the specification. Moreover, since these errors are believed to be errors of the Patent Office, such changes should be made at no expense to the Patentee. No new matter is added by these corrections.

In re Patent of
WRIGHT ET AL
Patent No. **US 7,103,049 B2**
Issued: **09/05/2006**

Please charge any shortage in fees due in connection with the filing of this paper to Deposit Account No. **01-0484** and please credit any excess fees to such deposit account.

Respectfully submitted,

/charles edmund wands/
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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO.	:	US 7,103,049 B2
DATED	:	September 5, 2006
INVENTOR(S)	:	Jonathan Aaron WRIGHT, Christopher A. OTTO

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below

Column 4, Line 53	Delete: "may occur very" Insert --may occur every--
Column 5, Line 11	Delete: "in order the for logic" Insert: --in order for the logic--
Column 10, Line 15	Delete: "and wherein step (a) comprises" Insert: --and wherein step (c) comprises--

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PATENT NO. US 7,103,049 B2

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